



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Inventor(S) Yih-Feng Chyan Case: 15-6-9

Serial No. 09/648164 Group Art Unit: 2826

Filing Date August 25, 2000 Examiner: Dickey

Title: Architecture For Circuit Connection Of A Vertical Transistor

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D. C. 20231

RESPONSE UNDER 37 C.F.R 1.111

SIR:

In response to the Office Action dated 11/5/03, kindly amend the above-identified application as indicated on the following pages:

Date of Deposit	3-5-04
I hereby certify that this Notice of Appeal is being deposited with the United States Postal Service "First Class Mail" on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
<u>Mary V Carter</u>	
(Printed name of person mailing paper or fee)	
<u>Mary V Carter</u>	
(Signature of person mailing paper or fee)	

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MAR 12 2004